



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Douglas E. Duschatko, Lane B. Quibodeaux, Robert A. Hall,  
Andrew J. Thurston  
Assignee: Cisco Technology, Inc.

Title: METHOD AND APPARATUS FOR DETECTING ERRORS IN  
A BACKPLANE FRAME

Application No.: 09/629,474 Filed: July 31, 2000

Examiner: Thien D. Tran Group Art Unit: 2665

Docket No.: CIS0009P2US Confirmation No.: 4017

Austin, Texas  
Date Mailed: April 27, 2005

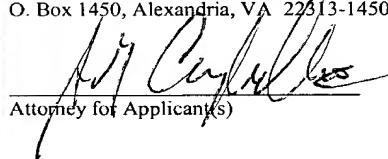
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Attention: Official Draftsperson  
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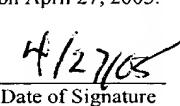
**SUBMISSION OF FORMAL DRAWINGS**

Dear Sir:

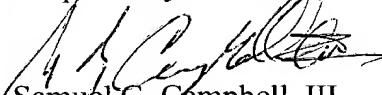
Applicants submit fifteen (15) sheets of formal drawings, consisting of Figures 1A; 1B; 2; 3; 4; 5; 6; 7; 8; 9; 10; 11; 12; 13 and 14 in the above-named application. If there are any questions regarding these drawings, please call the undersigned at (512) 439-5084.

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to Mail Stop Issue Fee, Commissioner For Patents, P. O. Box 1450, Alexandria, VA 22313-1450, on April 27, 2005.

  
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Attorney for Applicant(s)

  
\_\_\_\_\_  
Date of Signature  
4/27/05

Respectfully submitted,

  
\_\_\_\_\_  
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